PXD731x/PXD70xx High Voltage Waveform Digitizer Family





TECHNICAL DATA SHEET

PXI Features

High input voltage range with up to 500 V_{pp}

■ 100 MS/s with 16 Bit resolution

 Fully isolated design with up to two independent channels

 Multiple instrument and channel synchronization possibilities

- Built-in timer/counter engine for high speed timer/counter
- Built-in DVM function for high precision measurement

PXIe

cPCI

VXI

LAN

GPIB

USB

R\$232

external PCIe

Product Information

High Voltage, High Resolution Waveform Digitizer

The PXD731x High Voltage Waveform Digitizer family features up to two 100 MS/s simultaneously sampled input channels with 16 Bit resolution, input voltages up to ±250 V and a bandwidth up to 50 MHz.

Every digitizer channel has its own 2 MB memory which allows up to 1 million samples. Data can be acquired before and after the trigger event with a programmable sample counter, that controls the number of data points.

All PXD731x High Voltage Waveform Digitizer family devices have a high common mode rejection ratio (CMRR).

A great amount of trigger capabilities results in multiple instrument and channel synchronization possibilities.

Highest input voltage range allows easy measurement

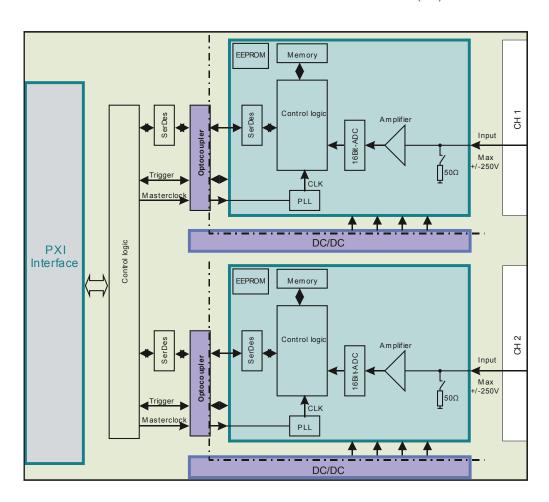
The maximum voltage for each signal input is ±250 V. This allows high voltage signals to be measured without additional signal conditioning.

High throughput design for many applications

The digitizers of the PXD731x family are designed for high throughput testing. This design guarantees highest quality measurements and is ideal for a wide range of application areas including automotive, communications, scientific applications, military/aerospace and consumer electronics.

Available with 1 and 2 channels

The single channel device PXD7313 High Voltage Waveform Digitizer uses only one PXI-slot (3U). The dual channel device PXD7314 High Voltage Waveform Digitizer needs two PXI-slots (3U).



General	Specification	Comment
Module size	1 slot, 3U 2 slots, 3U	PXD7xx3 PXD7xx4
Module weight	<0.4 kg <0.6 kg	PXD7xx3 PXD7xx4
Front connector type	BNC (isolated)	
Operating temperature	040°C	
Operating altitude	<2000 m	
Relative humidity	Up to 85% at 35°C	
Storage temperature range	-2570°C	
Electrical safety	According EN61010-1	
Isolation input to PE	250V CAT I, Pollution Degree 2	

Acquisition	Specification	Comment
Maximum sample rate	100 MS/s 40 MS/s 20 MS/s	See ordering information
Bandwidth Range 250mV, 500mV Range 1V, 2V, 4V All other ranges	>30 MHz >50 MHz >15 MHz	0.5 V _{pp} input signal; no filter 2 V _{pp} input signal; no filter 20 V _{pp} input signal; no filter
Vertical resolution	16 Bit	
Sampling times	10ns, 20ns, 50ns, 100ns, 200ns, 500ns, 1µs, 2µs, 5µs, 10µs, 20µs, 50µs, 100µs, 200µs, 500µs, 1ms, 2ms, 5ms, 10ms, 20ms, 50ms, 100ms, 200ms, 500ms, 1s, 2s, 5s, 10s	Software selectable
Input impedance	1MΩ // <20 pF, 50 Ω	Software selectable
Input coupling	DC	
Maximum input voltage	$\begin{array}{ll} 1M\Omega \colon f < 40\text{kHz} \colon & 250\text{V}_p \\ & 40\text{kHz} < f < 1\text{MHz} \colon 10^7\text{V}_p/f \\ & 1\text{MHz} < f < 50\text{MHz} \colon 10\text{V}_p \\ & 50\Omega \colon 5\text{V}_p \end{array}$	Input voltage may not exceed selected input voltage range
Input ranges	50 Ω: 250 mV, 500 mV, 1 V, 2 V, 4 V 1 MΩ: 250 mV, 500 mV, 1 V, 2 V, 4 V, 8 V, 16 V, 32 V, 64 V, 128 V, 250 V	
DC accuracy ¹	250 mV, 500 mV: 0.3% of input +2 mV others: 0.2% of input + 0.1% of full scale	
Filter	30 kHz, 100 kHz, 300 kHz, 1 MHz, 20 MHz	Software selectable
Waveform memory	2 MB, 1 MS	

DVM	Specification	Comment
DC accuracy ²	250mV, 500mV: 0.3% of input +0.5mV others: 0.2% of input + 0.025% of full scale	With auto offset correction
Measurement time	1500 ms	Software programmable

 $^{^{1}}$ DC accuracy specified for an average value of 100 samples with a sample rate of 5 kS/s and active 30 kHz filter. 2 DC accuracy specified with measurement time of 100 ms.

Notes: All product data are specified for 1 year at an ambient temperature of 23°C ±5°C (after 1 hour warm-up time). Product specification and description in this document are subject to change without notice.

Time Base	Specification	Comment
Accuracy	1ppm	In operating temperature range
Aging per year	1ppm	

Trigger System	Specification	Comment
Input from Internal function module Software PXI trigger	One function module can trigger itself and the other module Via software command Trigger 07 and star trigger	From the PXI backplane
Output to Internal function module PXI trigger	Output to the other module Output each channels trigger to PXI trigger 0 7	For example marker-bit
Level resolution	16 Bit	
Level accuracy	0.6% + 0.3%	±(of programmed value + of full range)
Trigger delay	010s	Programmable delay, 10 ns resolution
Trigger slope	Positive or negative	
Trigger hysteresis	0100% of signal range	Programmable via software
Pre-Trigger	0100% of full record length	Trigger is armed after all pre-samples are captured; post-samples are captured after trigger
Post-Trigger	0100% of full record length	Number of samples captured after trigger event

Timer/Counter

T/C Measurement Modes	Specification
Frequency Counter width Range Minimum pulse width	40 Bit 0.1 Hz 10 MHz 50 ns
Period Resolution Accuracy ^{1,2} Range	10 ns ±10 ns 50 ns 10 s
Time interval and pulse width Resolution Accuracy ^{1,2} Range	10 ns ±10 ns 50 ns 10 s
Rise and fall time Resolution Accuracy ² Range	10 ns ±10 ns 50 ns 10 s
Totalize (edge count) Minimum pulse width Range	50 ns 0 2 ⁴⁰ -1

 $^{^{1}}$ Square wave signal with $\rm T_{Rise}$ <1 ns and $\rm T_{Fall}$ <1 ns. 2 Trigger comparator error not included.

Available as 1 and 2 channel version





Ordering Information	Comment
PXD7313	100 MS/s, 1 channel
PXD7314	100 MS/s, 2 channel
PXD7023	40 MS/s, 1 channel
PXD7024	40 MS/s, 2 channel
PXD7013	20 MS/s, 1 channel
PXD7014	20 MS/s, 2 channel